

Vivado2019.1 安装

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1 文档简介

本文讲解如何安装 XILINX Vivado2019.1。

2 Vivado 简介

Vivado 设计套件, 是 FPGA 厂商赛灵思(XILINX)公司 2012 年发布的集成设计环境。ISE 是 Vivado 之前的上一代开发工具, 自 14.7 版本(对应 Vivado 2013.3)后已经停止开发了。Vivado 是 ISE 后的新一代开发工具, 运行时间更短, 对复杂设计更容易收敛。Vivado 软件可到 XILINX 官网 (www.xilinx.com) 下载或黑金开发板提供的资料中下载。下面介绍 Vivado 2019.1 软件的安装。

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Downloads

Vivado Installation Overview Video Licensing Help Alveo Acceleration Card Downloads

Vivado Embedded Development SDSoc Development Environment SDAccel Development Environment ISE Device Models CAE Vendor Libraries

Version

2019.1
2018.3
Archive

Vivado Design Suite - HLx Editions Update 1 - 2019.1

Important

Vivado Design Suite 2019.1.1 is now available with support for

- Production devices enabled:
 - Zynq UltraScale+ RFSoc (-2I, -2LI): XCZU39DR
 - XA Kintex-7 (-1Q): XA7K160T
 - XA Zynq UltraScale+ MPSoc (-1Q): XAZU7EV, XAZU11EG, XAZU7EG

For customers using these devices, Xilinx recommends installing Vivado 2019.1.1. For other devices, please continue to use Vivado 2019.1.

Download Includes: Vivado Design Suite HLx Editions (All Editions)

Last Updated: Jul 2, 2019

Answers: 2019.x - Vivado Known Issues

Documentation: Release Notes

Support Forums: Installation and Licensing

Download: Vivado HLx 2019.1 Update 1 (TAR/GZIP - 9.29 GB)

MD5 SUM Value: b943c251f10c0f3fa82838cd27092a38









Download Verification

3 安装步骤

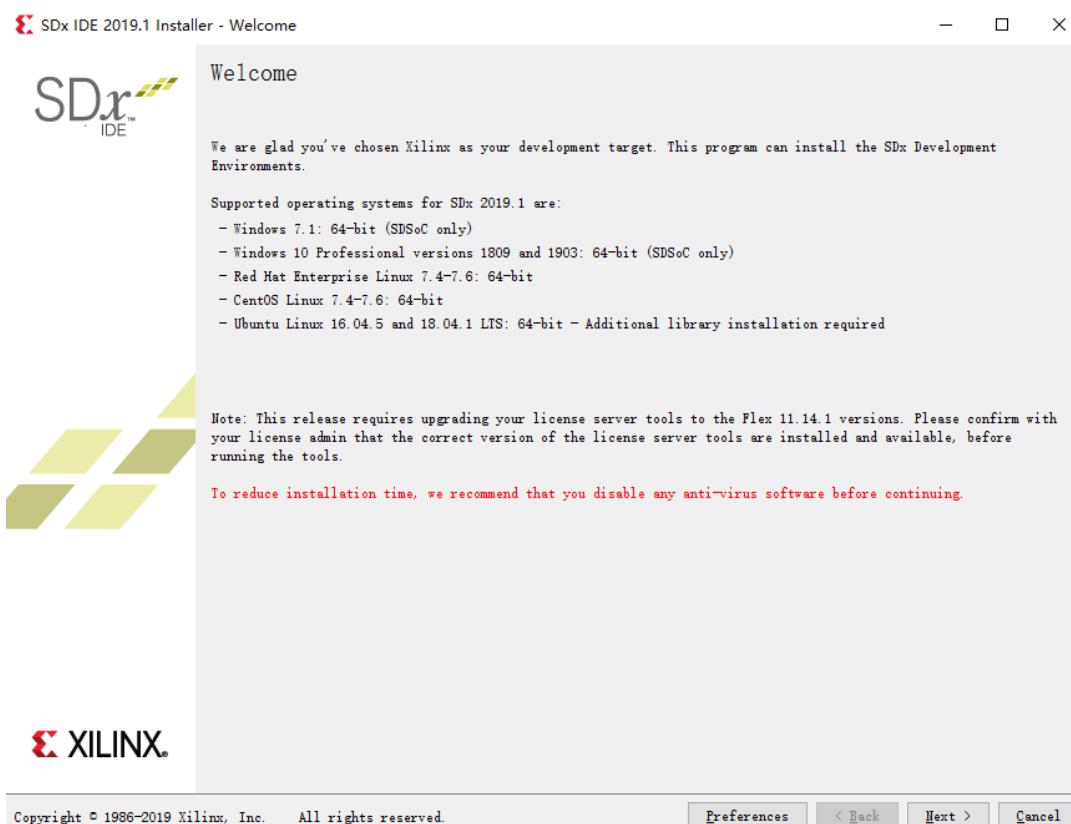
3.1 Vivado2019.1 安装

教程中 Vivado2019.1 安装过程是在 win10 64 位下进行的，在解压后的 vivado 软件中找到“xsetup.exe”安装文件。**注意：安装包不能解压在有中文路径的目录下。**

注意，在安装之前请把电脑上的所有杀毒软件关闭确保能够正常安装

	concrtdll	2019/5/30 9:46	应用程序扩展	328 KB
	msvcpl140.dll	2019/5/30 9:46	应用程序扩展	625 KB
	ucrtbase.dll	2019/5/30 9:46	应用程序扩展	960 KB
	ug973-vivado-release-notes-install-li...	2019/7/23 11:46	Foxit Reader PD...	2,559 KB
	vccorlib140.dll	2019/5/30 9:46	应用程序扩展	387 KB
	vcruntime140.dll	2019/5/30 9:46	应用程序扩展	88 KB
	xsetup	2019/5/30 9:46	文件	3 KB
	xsetup.exe	2019/5/30 9:46	应用程序	439 KB

1. 双击“xsetup.exe”出现如下界面，点击“Next”



2. 选中 3 个 I Agree (如下图) 后点击 “Next” ,

Accept License Agreements

Please read the following terms and conditions and indicate that you agree by checking the I Agree checkboxes.

Xilinx Inc. End User License Agreement

By checking "I Agree" below, or OTHERWISE ACCESSING, DOWNLOADING, INSTALLING or USING THE SOFTWARE, I AGREE on behalf of licensee to be bound by the agreement, which can be viewed by [clicking here](#).

I Agree

WebTalk Terms And Conditions

By checking "I Agree" below, I also confirm that I have read [Section 13 of the terms and conditions](#) above concerning WebTalk and have been afforded the opportunity to read the WebTalk FAQ posted at <https://www.xilinx.com/products/design-tools/webtalk.html>. I understand that I am able to disable WebTalk later if certain criteria described in Section 13(c) apply. If they don't apply, I can disable WebTalk by uninstalling the Software or using the Software on a machine not connected to the internet. If I fail to satisfy the applicable criteria or if I fail to take the applicable steps to prevent such transmission of information, I agree to allow Xilinx to collect the information described in Section 13(a) for the purposes described in Section 13(b).

I Agree

Third Party Software End User License Agreement

By checking "I Agree" below, or OTHERWISE ACCESSING, DOWNLOADING, INSTALLING or USING THE SOFTWARE, I AGREE on behalf of licensee to be bound by the agreement, which can be viewed by [clicking here](#).

I Agree

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< Back Next > Cancel

3. 这里保持默认勾选项，点击 “Next”

SDx Development Environments

Customize your installation by (de)selecting items in the tree below. Moving the cursor over the selections below provides additional information.

Choose the right devices for your target device. SDSoC = Zynq-7000 and Ultrascale+ MPSoC, SDAccel = 7 Series, Ultrascale, Ultrascale+ PCIe cards with an external host (e.g. x86)

- Design Tools
 - Software Defined Development Environment (SDx) IDE for SDSoC and SDAccel
 - DocNav
- Devices
 - Built-in Platforms and associated devices for SDSoC
 - Built-in Platforms and associated devices for SDAccel
 - Devices for Custom Platforms
 - SoCs
 - 7 Series
 - UltraScale
 - UltraScale+
 - Engineering Sample Devices for Custom Platforms
- Installation Options
 - Install Cable Drivers (You MUST disconnect all Xilinx Platform Cable USB II cables before proceeding)
 - Acquire or Manage a License Key
 - Enable WebTalk for SDx and Vivado to send usage statistics to Xilinx (Always enabled for WebPACK license)
 - Install WinPCap for Ethernet Hardware Co-simulation
 - Launch configuration manager to associate System Generator for DSP with MATLAB
 - Enable WebTalk for SDK to send usage statistics to Xilinx

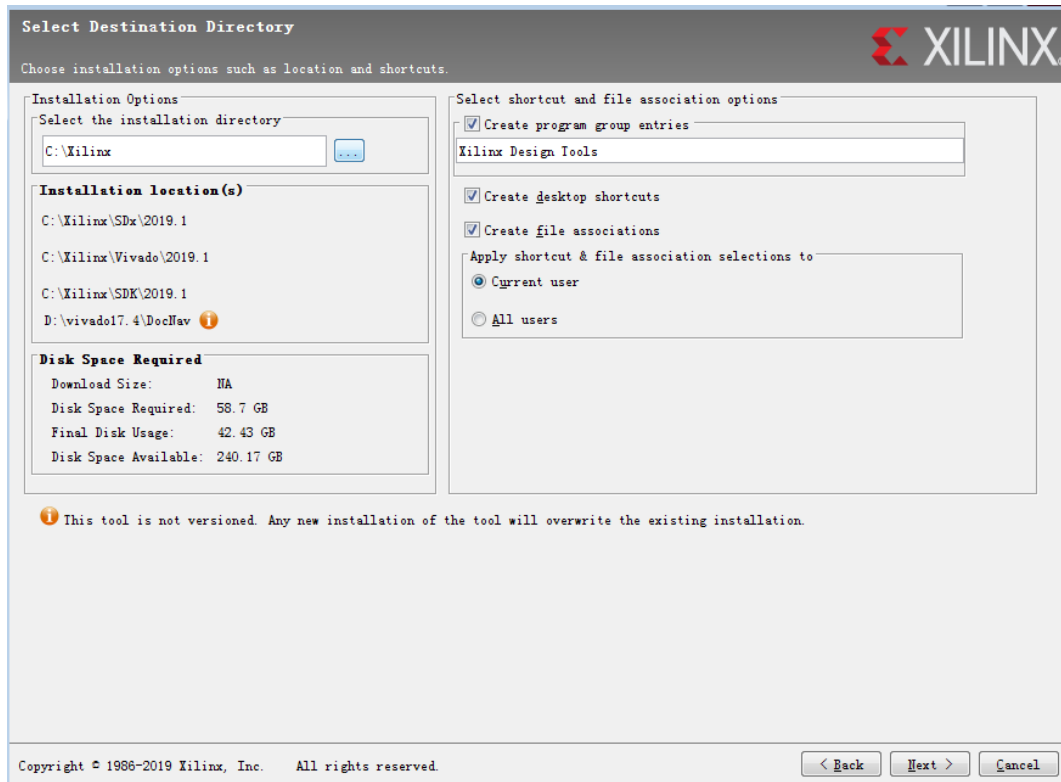
Download Size: NA
Disk Space Required: 58.7 GB

Reset to Defaults

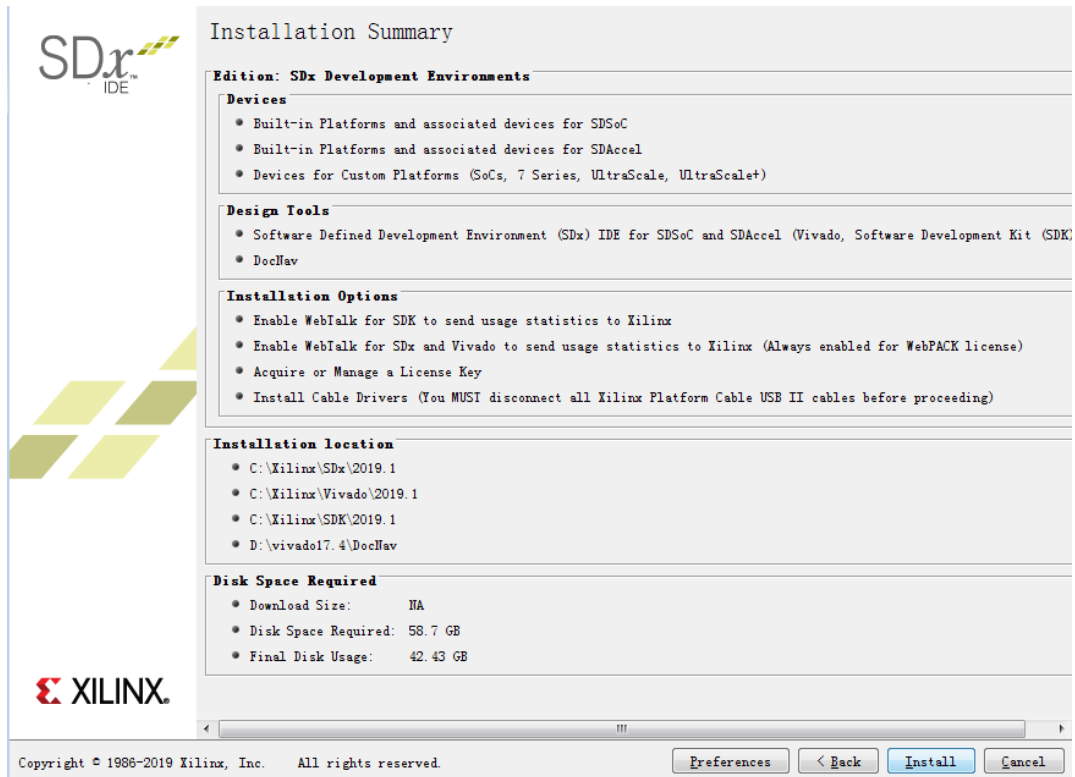
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< Back Next > Cancel

4. 确保所选的安装路径有足够的空间，保持默认，点击 “Next” ，如需修改路径不能有中文字符，空格等特殊字符，同时电脑的用户名不能有中文，否则安装后不能正确运行。



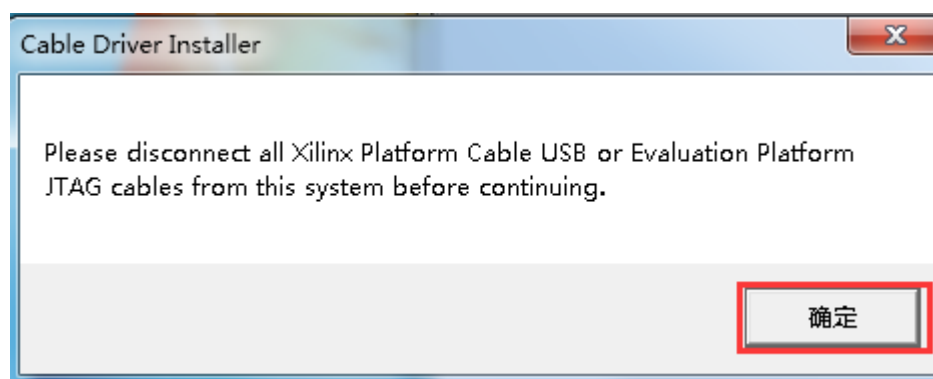
5. 在弹出如下界面前保持默认即可，点击“Install”



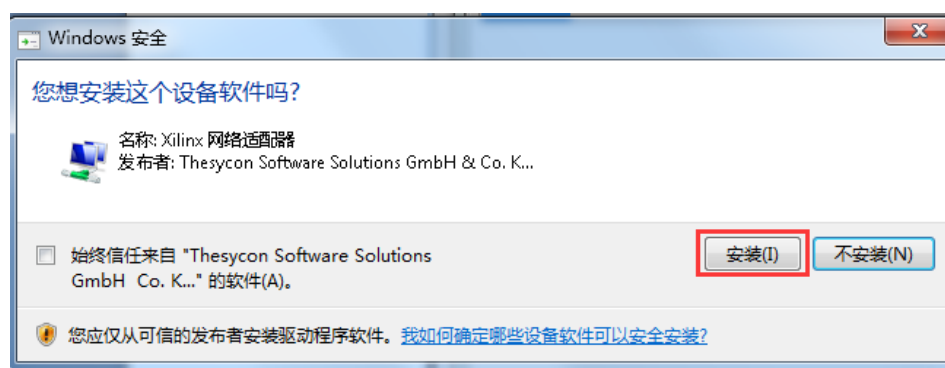
6. 在弹出如下界面前保持默认即可，安装需要一段时间，请耐心等待。



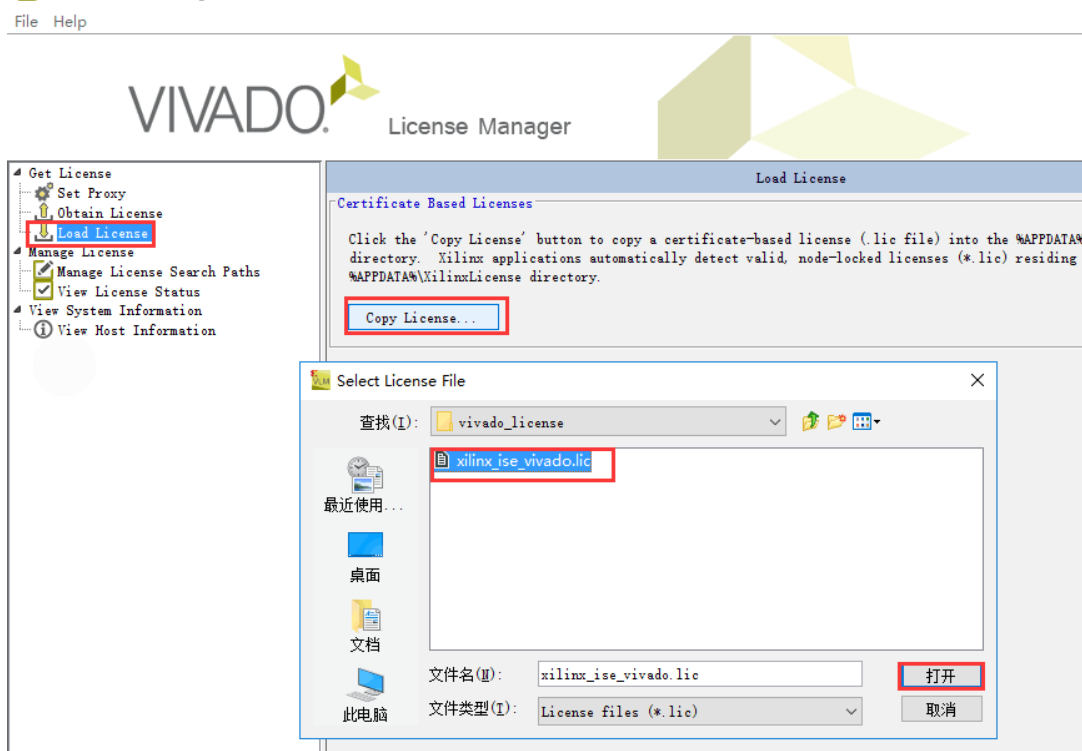
7. 在安装过程中弹出如下界面后，提示如果电脑已接上了 xilinx 的 FPGA 下载器，你要拔掉下载器再继续，如果没有接上下载器就不用管它，点击“确定”继续。



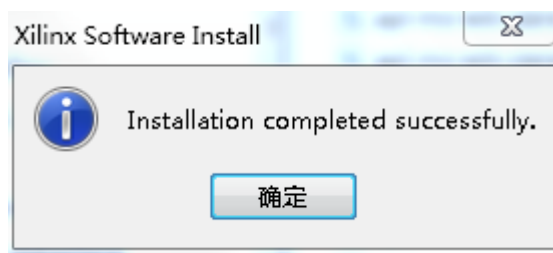
8. 在安装过程中弹出如下界面后，点击“安装”继续。



9. 安装 license，在开发板资料中找到 xilinx_ise_vivado.lic 文件进行安装。



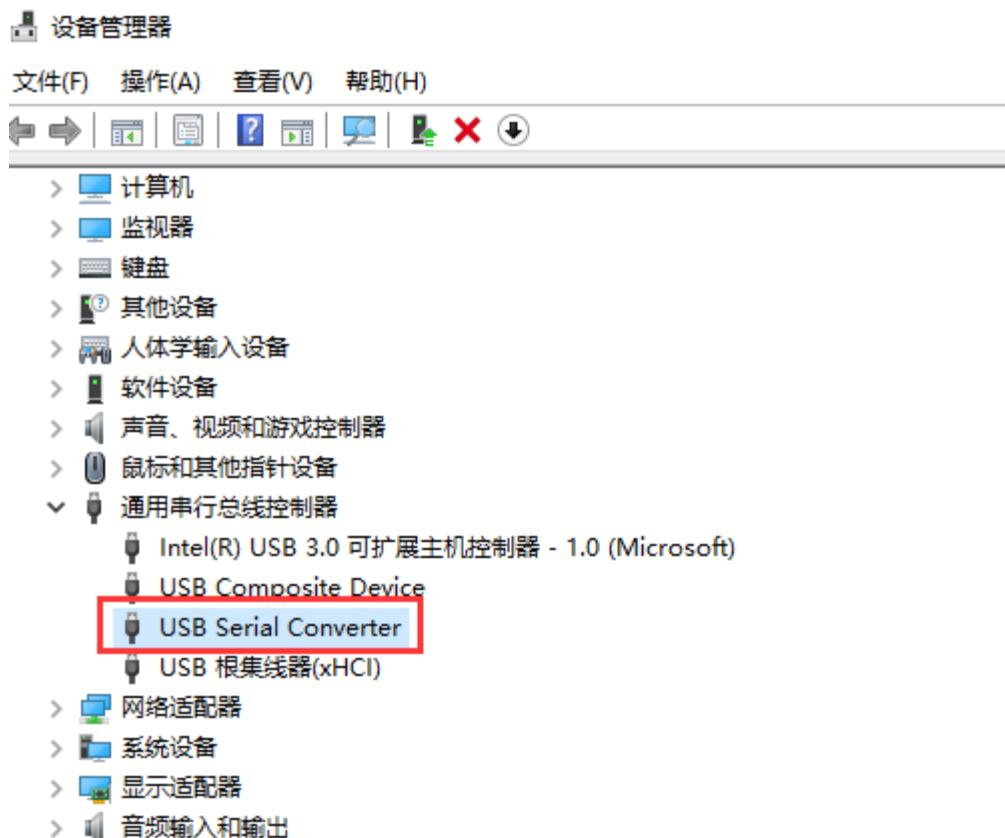
10. 安装完成后，点“确定”



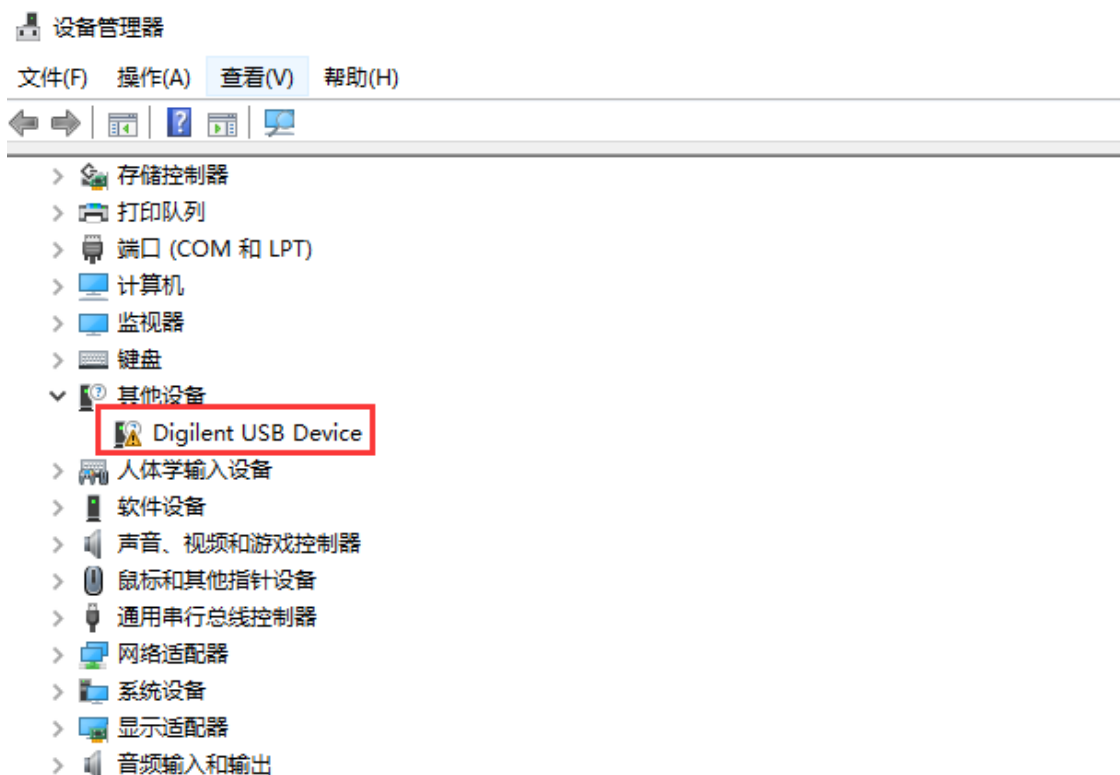
11. 安装完成后桌面上会显示如下图标。



12. 到此一般 VIVADO 安装后就可以工作了，因为 FPGA 的下载器驱动也会自动安装好，在设备管理器中可以找到，下图中显示了驱动安装成功与失败的情况，**当下载器驱动安装失败后，请自己参照 3.2 节内容进行安装。**



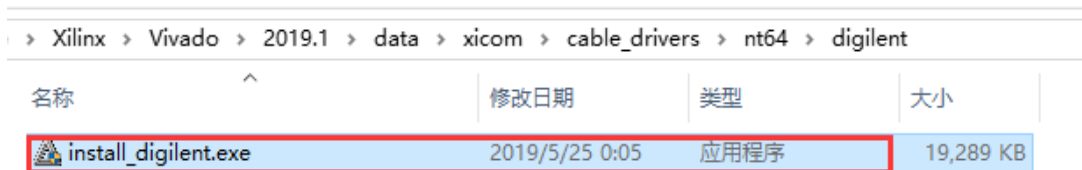
驱动安装成功



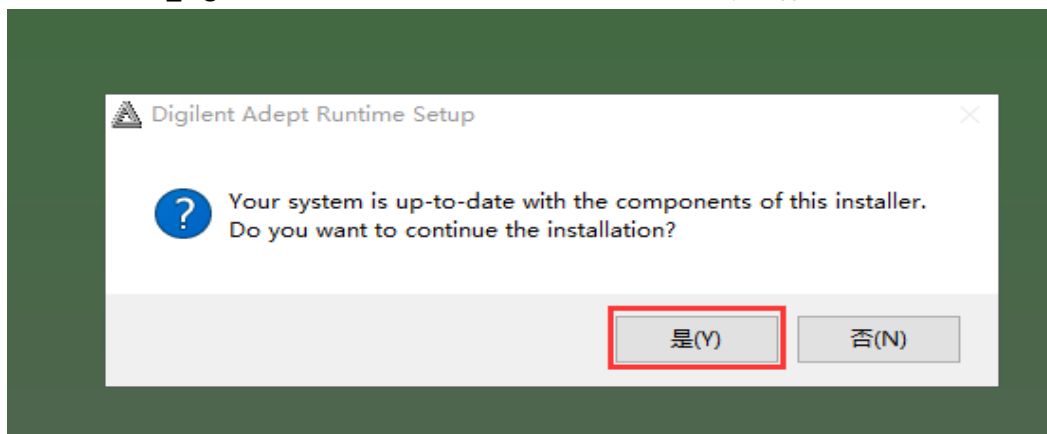
驱动安装失败

3.2 下载器驱动安装

1. 在软件安装目录路径中找到 install_digilent.exe 应用程序。这里以默认安装路径为例如下图所示：



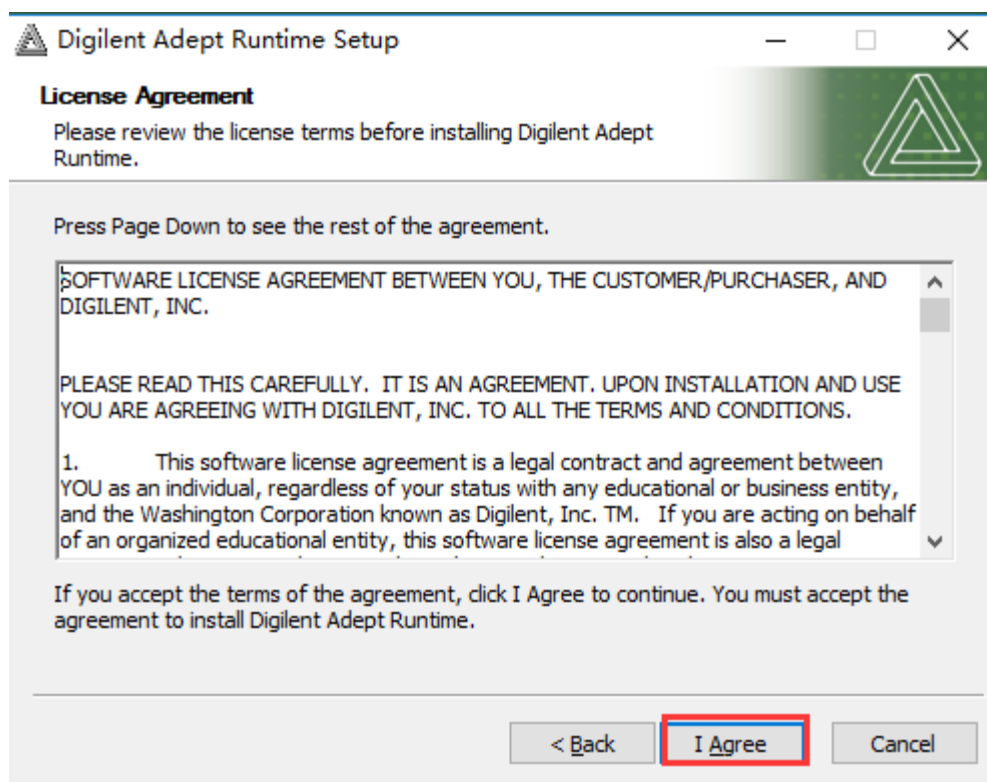
2. 双击 install_digilent.exe 后，会弹出安装界面，点击“是”，开始安装：



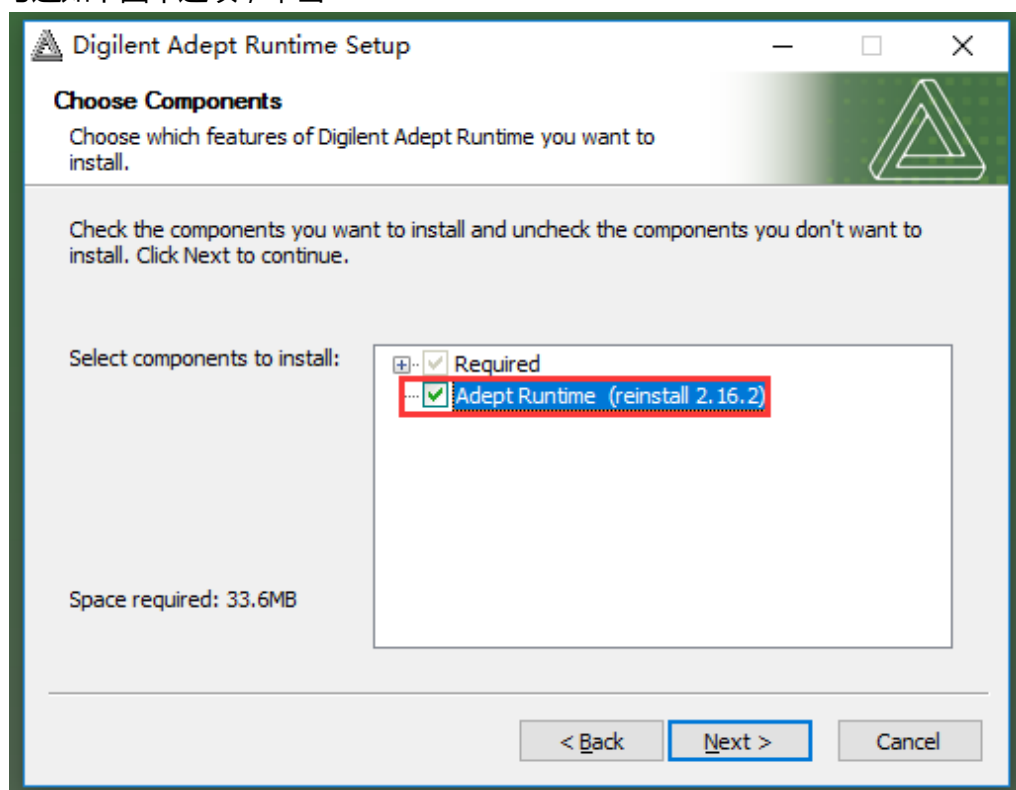
3. 单击“Next”：



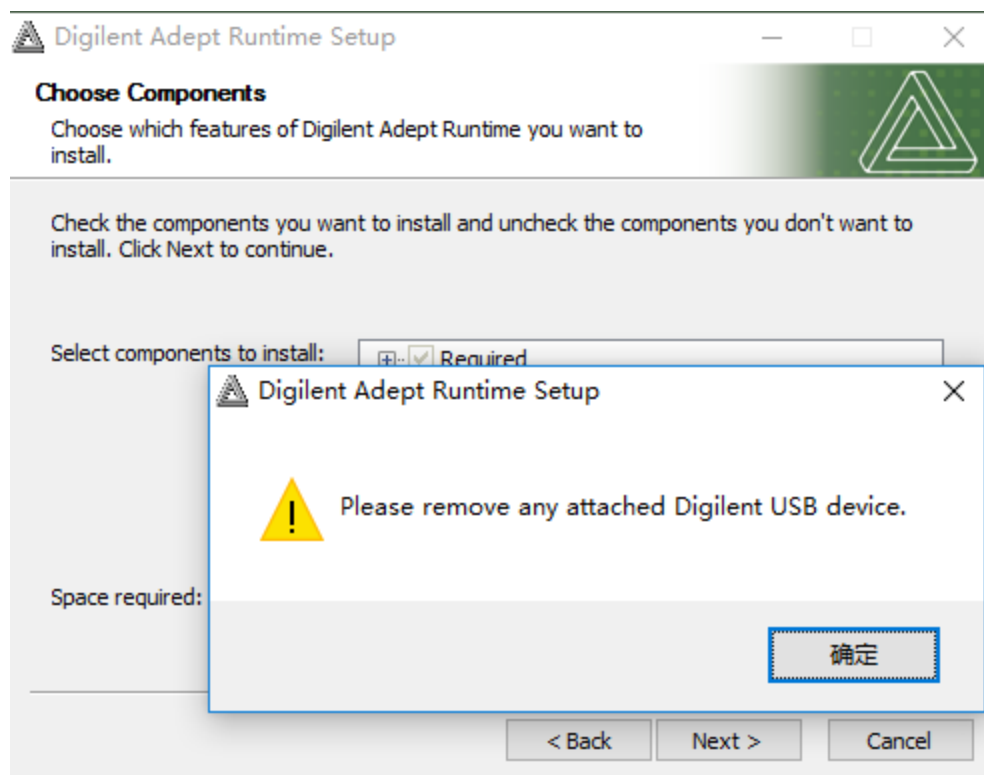
4. 单击“I Agree”：



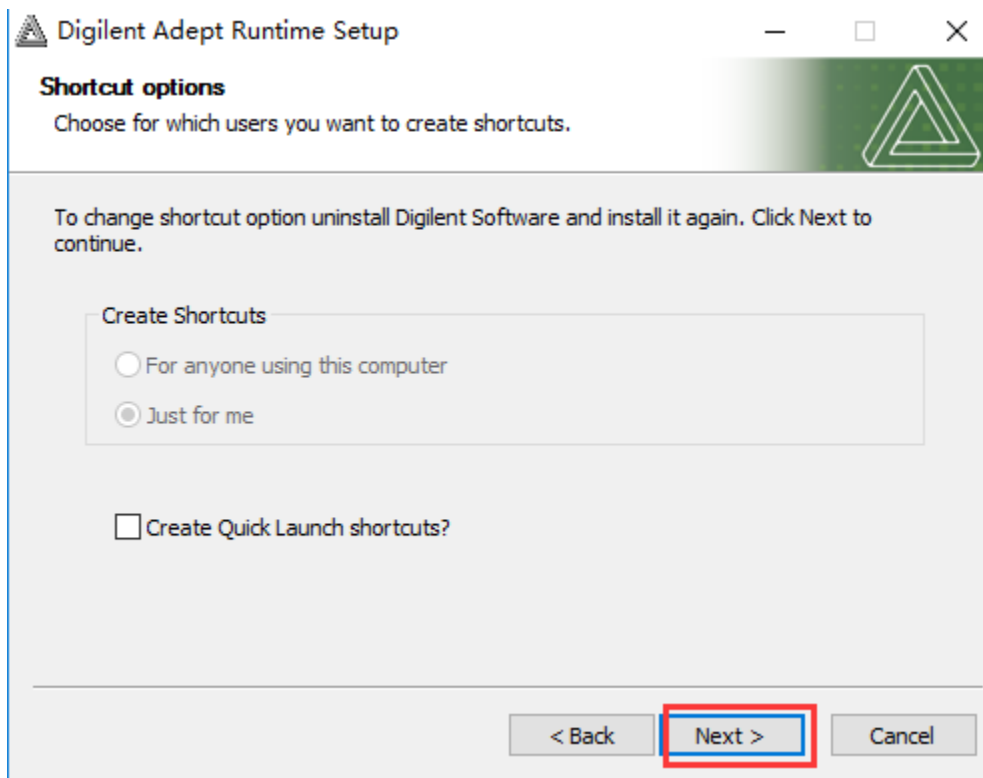
5. 勾选如下图中选项，单击“Next”：



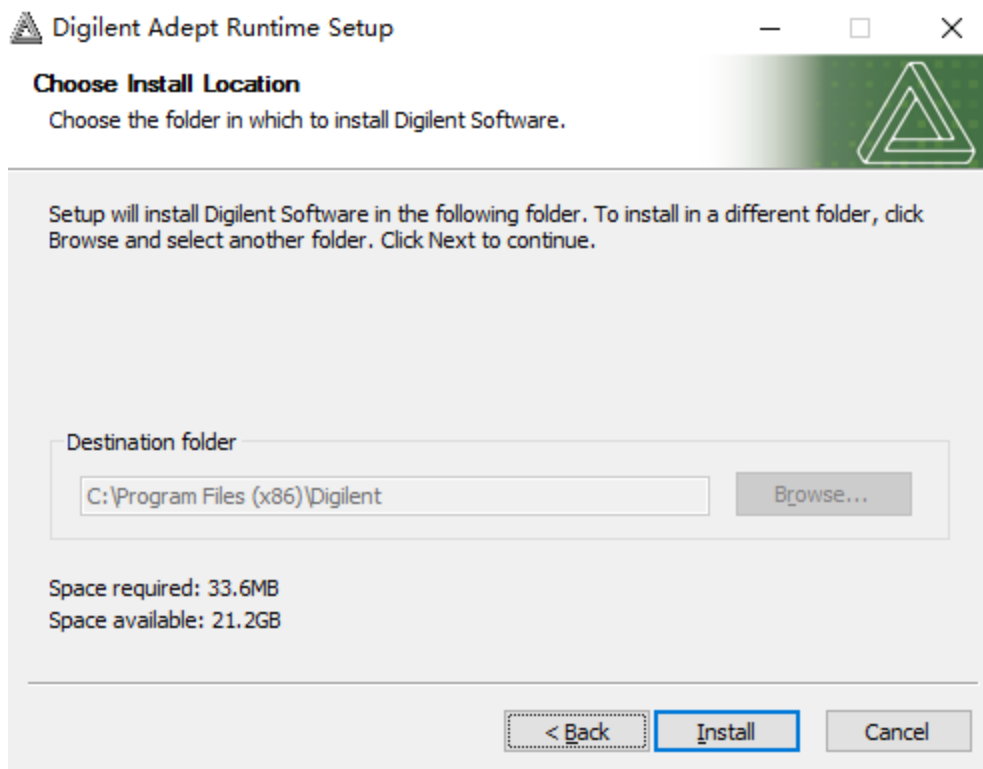
6. 会提示您拔掉 Xilinx USB Cable,确认连接电脑的 USB 线的下载器是否拔下了,点击“确定”。



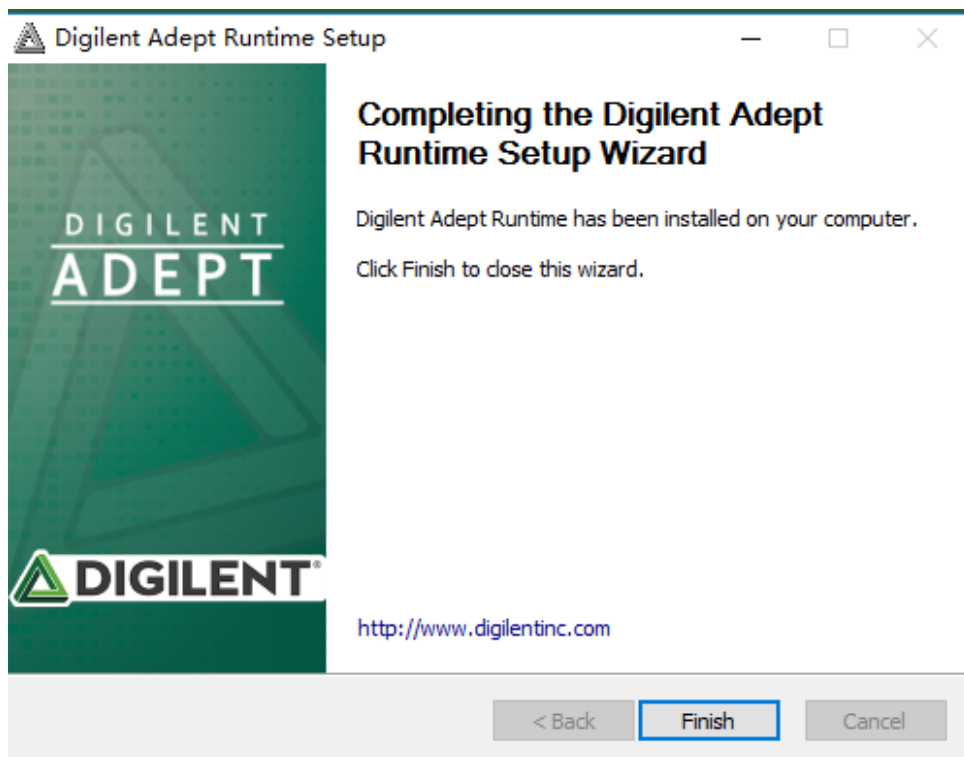
7. 这里默认设置,不创建快捷方式,单击“Next”:



8. 点击 “Install” 按钮



9. 直到安装完成, 点击“ finish” .



10. 再用 USB 线连接电脑和下载器，在设备管理器里就能找到 USB Serial Converter 设备了,如下图所示，到此驱动安装完毕。

